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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10064306	FILING DATE 07/01/2002	CLASS 344	SUBCLASS 205	GAU 2186	EXAMINER AUDUONG
<b>**APPLICANTS:</b> Brennan Ciaran; Fifield John; Stephens Jeremy; Storaska Daniel; 365 7818					
<b>**CONTINUING DATA VERIFIED:</b> None					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> None					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no				FIS920010334	
Verified and Acknowledged Examiners's initials <i>SA</i>					
TITLE : Writeback and refresh circuitry for direct sensed DRAM macro					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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